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NOTICE OF ALLOWANCE AND FEE(S) DUE

26211

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04/07/2004

FISH & RICHARDSON P.C. 45 ROCKEFELLER PLAZA, SUITE 2800 NEW YORK, NY 10111 EXAMINER KEBEDE, BROOK

PAPER NUMBER

ART UNIT

DATE MAILED: 04/07/2004

	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
•	09/970,013	10/02/2001	Noriaki Sakamoto	10417-102001	9407

TITLE OF INVENTION: METHOD OF MANUFACTURING CIRCUIT DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/07/2004

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATEN <u>PROSECUTION ON THE MERITS IS CLOSED</u>. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHT THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPO PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM TH MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THE STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOV REFLECTS A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE APPLIED IN THIS APPLICATION. THE PTOL-85B (O AN EQUIVALENT) MUST BE RETURNED WITHIN THIS PERIOD EVEN IF NO FEE IS DUE OR THE APPLICATION WIL BE REGARDED AS ABANDONED.

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If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

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B. If the status is changed, pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above and notify the United States Patent and Trademark Office of the change in status, or

If the SMALL ENTITY is shown as NO:

- A. Pay TOTAL FEE(S) DUE shown above, or
- B. If applicant claimed SMALL ENTITY status before, or is n claiming SMALL ENTITY status, check the box below and enclo the PUBLICATION FEE and 1/2 the ISSUE FEE shown above.
- Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- II. PART B FEE(S) TRANSMITTAL should be completed and returned to the United States Patent and Trademark Office (USPTO) w your ISSUE FEE and PUBLICATION FEE (if required). Even if the fee(s) have already been paid, Part B Fee(s) Transmittal should completed and returned. If you are charging the fee(s) to your deposit account, section "4b" of Part B Fee(s) Transmittal should completed and an extra copy of the form should be submitted.
- III. All communications regarding this application must give the application number. Please direct all communications prior to issuance Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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Complete and send this form, together with applicable fee(s), to: Mail

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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maintenance fee notifications.

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Note: A certificate of mailing can only be used for domestic mailings of Fee(s) Transmittal. This certificate cannot be used for any other accompany papers. Each additional paper, such as an assignment or formal drawing, m have its own certificate of mailing or transmission.

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I hereby certify that this Fee(s) Transmittal is being deposited with the Un States Postal Service with sufficient postage for first class mail in an envel addressed to the Mail Stop ISSUE FEE address above, or being facsim transmitted to the USPTO, on the date indicated below.

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nonprovisional NO		\$1330		\$300	\$1630	07/07/2004
EXAMINER		ART UNIT		CLASS-SUBCLASS		
KEBEDE, BROOK				438-110000	_	
Change of correspondence address or indication of "Fee Address" (37 CFR 1.363). Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached. "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.				inting on the patent front page f up to 3 registered patent a R, alternatively, (2) the name ing as a member a registered d the names of up to 2 regis or agents. If no name is liste inted.	attorneys or I of a single attorney or 2 tered patent	

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categ	ories (will not be printed on the patent);	individual 🗅	corporation or other private group entity	☐ governm
4a. The following fee(s) are enclosed:	4b. Payment of Fee(s):		•	
☐ Issue Fee	☐ A check in the amo	unt of the fee(s)	is enclosed.	
☐ Publication Fee	☐ Payment by credit of	ard. Form PTO-	2038 is attached.	
☐ Advance Order - # of Copies	The Director is he Deposit Account Num	reby authorized iber	by charge the required fee(s), or credit any (enclose an extra copy of this	overpayment form).

tor for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

(Authorized Signature)

(Date)

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This collection of information is required by 37 CFR 1.311. The USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Alexandria, Virginia 22313-1450.

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45 ROCKEFELLE NEW YORK, NY	R PLAZA, SUITE 2800		ART UNIT	PAPER NUMBER		
11211 10144,111	••••		2823			

DATE MAILED: 04/07/2004

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 158 day(s). If the issue fee is paid on the date that is three months after t mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a ha months) after the mailing date of this notice, the Patent Term Adjustment will be 158 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date th determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retriev (PAIR) system (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office Patent Legal Administration at (703) 305-1383. Questions relating to issue and publication fee payments should directed to the Customer Service Center of the Office of Patent Publication at (703) 305-8283.



	Application No.	Applicant(s)
	09/970,013	SAKAMOTO ET AL.
Notice of Allowability	Examiner	Art Unit
	Brook Kebede	2823
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED or other appropriate comm GHTS. This application is	in this application. If not included nunication will be mailed in due course. THIS
1. This communication is responsive to <u>02 January 2004</u> .		
2. The allowed claim(s) is/are 1-27.		
3. \boxtimes The drawings filed on <u>02 October 2001</u> are accepted by the	e Examiner.	
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have Topies of the certified copies of the priority documents have all the	been received. been received in Applicat	ion No
Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		le a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give		
 CORRECTED DRAWINGS (as "replacement sheets") mus (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the sheet. Replacement sheet(s) should be labeled as such in the deposit of the sheet of the	on's Patent Drawing Reviews S Amendment / Comment of the second of the s	or in the Office action of the drawings in the front (not the back) of FR 1.121(d). FERIAL must be submitted. Note the
 Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 1/2/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☑ Interview S Paper No 8), 7. ☑ Examiner	nformal Patent Application (PTO-152) Summary (PTO-413), ./Mail Date s Amendment/Comment s Statement of Reasons for Allowance

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EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Paul A. Levy on March 24, 2004.

2. The application has been amended as follows:

In the Claims:

In Claim 1, lines 1-2, change "A method of manufacturing a circuit device comprising the steps of:" to -- A method of manufacturing a circuit device comprising:--.

In Claim 1, lines 3-5, change "preparing a conductive foil and forming an isolation trench having a smaller thickness than that of the conductive foil on the conductive foil in a region excluding where a conductive pattern of a first layer is to be formed, thereby forming the conductive pattern of the first layer;" to -- preparing a conductive foil and forming an isolation trench having a smaller thickness than that of the conductive foil <u>in</u> the conductive foil, thereby forming <u>a</u> conductive pattern of <u>a</u> first layer;--.

In Claim 1, lines 9-12, change "mounting a circuit element onto the conductive pattern; covering the circuit element and entirely molding with an insulating resin; and removing the conductive foil in a portion where the isolation trench is not provided." to -- mounting <u>at least one</u> circuit

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element onto the conductive pattern; covering the circuit element and entirely molding with an insulating resin; and removing the conductive foil in a portion where the isolation trench is not formed.--.

In Claim 2, lines 1-2, change "A method of manufacturing a circuit device according to claim 1 further comprising the step of:" to -- A method of manufacturing a circuit device according to claim 1 further comprising:--.

In Claim 4, lines 2-3, change "wherein the isolation trench to be selectively formed on the conductive foil is provided through chemical or physical etching." to – wherein the isolation trench **is** selectively formed **in** the conductive foil **by** chemical or physical etching.--.

In Claim 6, lines 1-2, change "wherein a via hole is formed on the interlayer insulating film through a laser." to --wherein a via hole is formed <u>in</u> the interlayer insulating film <u>by</u> a laser.--.

In Claim 11, lines 2-3, change "wherein the circuit element has either or both of a semiconductor bare chip and a chip circuit component fixed thereto." to -- wherein the circuit element comprises at least one of a semiconductor bare chip and a chip circuit component.--.

In Claim 13, lines 1-10, change "A method of manufacturing a circuit device comprising the steps of: preparing a conductive foil; providing an interlayer insulating film over the conductive foil; providing plural layers of a conductive pattern over the interlayer insulating film; mounting

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a circuit element onto the conductive pattern; covering the circuit element and molding a whole surface with an insulating resin; and removing the conductive foil." to --A method of manufacturing a circuit device comprising: preparing a conductive foil and forming an isolation trench in the conductive foil thereby forming a conductive pattern; providing an interlayer insulating film pattern over the conductive foil; providing plural layers of a conductive patterns over the interlayer insulating film pattern; mounting at least one circuit element onto at least one of the plural layers of the conductive patterns; covering the circuit element and molding a whole surface with an insulating resin; and removing the conductive foil in a portion where the isolation trench is not formed.--.

In Claim 14, lines 1-4, change "A method of manufacturing a circuit device according to claim 13 further comprising the step of: isolating the insulating resin through dicing for each circuit device including the circuit element." to -- A method of manufacturing a circuit device according to claim 13 further comprising: isolating the insulating resin through dicing for each circuit device including the circuit element.--.

In Claim 22, lines 1-3, change "The method of manufacturing a circuit device according to claim 13, wherein the circuit element has either or both of a semiconductor bare chip and a chip circuit component fixed thereto." to -- The method of manufacturing a circuit device according to claim 13, wherein the circuit element comprises at least one of a semiconductor bare chip and a chip circuit component.--.

3. Claims 1-27 are allowed over prior art of record.

Reasons for Allowance

The prior art of record neither anticipates nor renders obvious the claimed subject matter of the instant application as a whole either taken alone or in combination, in particular, prior art of record does not teach "removing the conductive foil in a portion where the isolation trench is not formed," as recited in claims 1 and 13 respectively.

Claims 2-12 and 14-27 are also allowed as being dependent of the allowed independent base claim.

Conclusion

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Correspondence

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brook Kebede whose telephone number is (571) 272-1862. The examiner can normally be reached on 8-5 Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Olik Chaudhuri can be reached on (571) 272-1855. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

BK March 31, 2004

Primary Examiner